Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination MURANAKA, KOJI | Examiner | Art Unit | Tuan Quach | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name .	Classification
	Α	US-6,329,062	12-2001	Gaynor, Justin F.	428/447
	В	US-2003/0031877	02-2003	Veerasamy et al.	428/426
	С	US-4,714,636	12-1987	Yokono et al.	428/1.31
	D	US-			
	Е	US-			
	F	US-			*
	G	US-			
	Ξ	US-	·		
	-	US-			
	J.	US-			
	к	US-			
	L	US-	·		
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р				,	
	Q		•			
	R					
	S					
	Ť					-

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., Silicon Processing for the VLSI Era, Vol. 2, Lattice Press, 1990, pp. 273-283.
	٧	
	W	·
	x	

^{*}A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.